Understanding Integer Overflow in C/C++

WILL DIETZ, University of Illinois at Urbana-Champaign
PENG LI and JOHN REGEHR, University of Utah
VIKRAM ADVE, University of Illinois at Urbana-Champaign

Integer overflow bugs in C and C++ programs are difficult to track down and may lead to fatal errors or exploitable vulnerabilities. Although a number of tools for finding these bugs exist, the situation is complicated because not all overflows are bugs. Better tools need to be constructed, but a thorough understanding of the issues behind these errors does not yet exist. We developed IOC, a dynamic checking tool for integer overflows, and used it to conduct the first detailed empirical study of the prevalence and patterns of occurrence of integer overflows in C and C++ code. Our results show that intentional uses of wraparound behaviors are more common than is widely believed; for example, there are over 200 distinct locations in the SPEC CINT2000 benchmarks where overflow occurs. Although many overflows are intentional, a large number of accidental overflows also occur. Orthogonal to programmers' intent, overflows are found in both well-defined and undefined flavors. Applications executing undefined operations can be, and have been, broken by improvements in compiler optimizations. Looking beyond SPEC, we found and reported undefined integer overflows in SQLite, PostgreSQL, SafeInt, GNU MPC and GMP, Firefox, LLVM, Python, BIND, and OpenSSL; many of these have since been fixed.

Categories and Subject Descriptors: D.3.3 [Programming Languages]: Language Constructs and Features; D.3.0 [Programming Languages]: General—Standards; D.2.5 [Software Engineering]: Testing and Debugging—Diagnostics; D.2.4 [Software Engineering]: Software/Program Verification—Validation

General Terms: Experimentation, Languages, Measurement, Performance

Additional Key Words and Phrases: Integer overflow, integer wraparound, undefined behavior

ACM Reference Format:
DOI: http://dx.doi.org/10.1145/2743019

1. INTRODUCTION

Integer numerical errors in software applications can be insidious, costly, and exploitable. These errors include overflows, value-losing conversions (e.g., a truncating cast of an int to a short in C++ that results in the value being changed), and illegal uses of operations such as shifts (e.g., shifting a value in C by at least as many positions as its bitwidth). We refer to all these types of errors collectively as “overflow errors.” These errors can lead to serious software failures, for example, a truncation error on a cast of a floating point value to a 16-bit integer played a crucial role in the destruction of Ariane 5 flight 501 in 1996. These errors are also a source of serious vulnerabilities, such as integer overflow errors in OpenSSH [MITRE Corporation 2002] and Firefox [MITRE...
Corporation 2010], both of which allow attackers to execute arbitrary code. In their 2011 report, MITRE places integer overflows in the “Top 25 Most Dangerous Software Errors” [Christey et al. 2011].

Detecting integer overflows is relatively straightforward by using a modified compiler to insert runtime checks. However, reliable detection of overflow errors is surprisingly difficult because overflow behaviors are not always bugs. The low-level nature of C and C++ means that bit- and byte-level manipulation of objects is commonplace; the line between mathematical and bit-level operations can often be quite blurry. Wraparound behavior using unsigned integers is legal and well-defined, and there are code idioms that deliberately use it. On the other hand, C and C++ have undefined semantics for signed overflow and shift past bitwidth—operations that are perfectly well-defined in other languages such as Java. C/C++ programmers are not always aware of the distinct rules for signed versus unsigned types and may naively use signed types in intentional wraparound operations. If such uses were rare, compiler-based overflow detection would be a reasonable way to detect integer overflow errors. If it is not rare, however, such an approach would be impractical, and more sophisticated techniques would be needed to distinguish intentional uses from unintentional ones.

Although it is commonly known that C and C++ programs contain numerical errors and also benign, deliberate use of wraparound, it is unclear how common these behaviors are and in what patterns they occur. In particular, there is little data available in the literature to answer the following questions.

1. How common are numerical errors in widely-used C/C++ programs?
2. How common is the use of intentional wraparound operations with signed types—which has undefined behavior—relying on the fact that today’s compilers may compile these overflows into correct code? We refer to these overflows as time bombs because they remain latent until a compiler upgrade turns them into observable errors.
3. How common is intentional use of well-defined wraparound operations on unsigned integer types?

Although there have been a number of papers on tools for detecting numerical errors in C/C++ programs, no previous work we know of has explicitly addressed these questions or contains sufficient data to answer any of them. The closest is the work of Brumley et al. [2007], which presents data to motivate the goals of the tool and also to evaluate false positives (invalid error reports) due to intentional wraparound operations. As discussed in Section 8, that paper only tangentially addresses the third point previously listed. We study all of these questions systematically.

This article expands upon our previous work [Dietz et al. 2012], making the following primary contributions: First, we developed Integer Overflow Checker (IOC), an open-source tool that detects both undefined integer behaviors as well as well-defined wraparound behaviors in C/C++ programs. Second, we present the first detailed, empirical study—based on SPEC 2000, SPEC 2006, and a number of popular open-source applications—of the prevalence and patterns of occurrence of numerical overflows in C/C++ programs. Part of this study includes a manual analysis of a large number of intentional uses of wraparound in a subset of the programs. Third, we used IOC to discover previously unknown overflow errors in widely-used applications and libraries,

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1 In fact, in the course of our work, we have found that even experts writing safe integer libraries or tools to detect integer errors sometimes make mistakes due to the subtleties of C/C++ semantics for numerical operations.
2 IOC is available as part of the production version of the Clang compiler for C/C++ [Clang 2011] as of version 3.3. Usage information and list of available checks are available at http://clang.llvm.org/docs/ UsersManual.html. The original IOC as described in Section 4 is also available at http://embed.cs.utah.edu/ioc/.
including SQLite, PostgreSQL, BIND, Firefox, OpenSSL, GCC, LLVM, the SafeInt library, the GNU MPC and GMP libraries, Python, and PHP. A number of these have been acknowledged and fixed by the maintainers (see Section 6).

In addition, this article makes the following new contributions: First, we conducted a new large-scale experiment in which we automatically tested for overflows in 1,172 of the top 10,000 Debian packages. We find that 35% of these packages triggered an overflow during execution of their test suites, and 16% invoked overflows with undefined semantics. These results provide strong new evidence that a substantial fraction of arbitrary C/C++ software packages, including many well-tested ones, contain serious overflow errors. Second, we improved IOC by implementing and evaluating recoverability and two optimizations described in Section 4.4. Third, we integrate IOC into mainline Clang, a widely-used production C/C++ compiler (e.g., it is the primary system compiler on Mac OS and iOS). The features of IOC are available via the \(-fsanitize=*\) family of options (e.g., \(-fsanitize=integer\) and \(-fsanitize=shift\)). This deployment led to improvements in diagnostics and the addition of new features, such as error deduplication, to meet user needs. We describe this experience in Section 5, contributing the discussion of our end-to-end development of an effective bug-detection tool that can now be easily used by LLVM/Clang users. Fourth, this article contains additional discussion of relevant details including hardware support for integer arithmetic (Section 2), language rules governing implementation-defined behavior (Section 3.1), and an explanation of the usual arithmetic conversions (Section 3.2). Fifth, we evaluate the performance overhead imposed by IOC under different checking policies on integer-intensive CPU benchmarks as well as security-sensitive server software. These results are presented in Section 7.

The key findings from our study of overflows are as follows. First, all four combinations of intentional and unintentional, well-defined and undefined integer overflows occur frequently in real codes. For example, the SPEC CINT2000 benchmarks had over 200 distinct occurrences of intentional wraparound behavior, for a wide range of purposes. Some uses for intentional overflows are well known, such as hashing, cryptography, random number generation, and finding the largest representable value for a type. Others are less obvious, for example, inexpensive floating point emulation, negation of \(\text{INT}_\text{MIN}\), and even ordinary multiplication and addition. We present a detailed analysis of examples of each of the four major categories of overflow. Second, overflow-related issues in C/C++ are very subtle and we find that even experts get them wrong. For example, the latest revision of Firefox (as of September 1, 2011) contained integer overflows in the library that was designed to handle untrusted integers safely in addition to overflows in its own code. More generally, we found very few mature applications that were completely free of integer numerical errors. This implies that there is probably little hope of eliminating overflow errors in large code bases without sophisticated tool support. However, these tools cannot simply distinguish errors from benign operations by checking rules from the ISO language standards. Rather, tools will have to use sophisticated techniques and/or rely on manual intervention (e.g., annotations) to distinguish intentional from unintentional overflows.

### 2. HARDWARE INTEGER ARITHMETIC

Integer overflow bugs in C and C++ find their roots in features and limitations of the underlying hardware platform. On modern processors, an \(n\)-bit unsigned integer is represented in such a way that its value is

\[
\sum_{i=0}^{n-1} x_i 2^i,
\]
where $x_i$ is the value of the $i$th bit. The range of an $n$-bit unsigned integer is $0 \ldots 2^n - 1$. Signed integers on all modern platforms use a two’s complement representation, where the value of an integer is

$$-x_{n-1}2^{n-1} + \sum_{i=0}^{n-2} x_i 2^i.$$

An advantage of two’s complement is that signed and unsigned addition can be performed using the same operation. The same is true for subtraction and multiplication. Historically, this was advantageous because fewer instructions needed to be implemented. Also, unlike the ones’ complement and sign-magnitude representations, two’s complement has only one representation for zero. A drawback of two’s complement is that its range, $-2^{n-1} \ldots 2^{n-1} - 1$, is asymmetric. Thus, there is a representable value, $-2^{n-1}$, that does not have a representable additive inverse—a fact that programmers can and do forget.

When an $n$-bit addition or subtraction operation on unsigned or two’s complement integers overflows, the result “wraps around,” effectively subtracting $2^n$ from, or adding $2^n$ to, the true mathematical result. Equivalently, the result can be considered to occupy $n + 1$ bits; the lower $n$ bits are placed into the result register and the highest-order bit is placed into the processor’s carry flag.

Unlike addition and subtraction, the result of an integer multiplication can wrap around many times. Consequently, a processor typically places the result of an $n$-bit multiplication into a location that is $2n$ bits wide, such as a pair of registers. Division instructions commonly have an analogous structure where the dividend is $2n$ bits wide, the divisor is $n$ bits, and the quotient and remainder are both $n$ bits. Two’s complement division (unlike addition, subtraction, and multiplication) is not performed by the same operation as unsigned division of the same width. For example, the x86 architecture has both the idiv instruction for two’s complement integer division and div for unsigned integer division.

Processors intended for digital signal processing, including dedicated DSPs and also SIMD extensions to general-purpose processors, support saturating semantics for overflowing integer operations. Saturating arithmetic never wraps around but rather “sticks” at the maximum or minimum representable value. In some domains, saturation is a reasonable solution to the problem of integer overflow, but since saturating semantics are not used by C or C++, we will not consider it further.

Processor-level left-shift instructions typically place zeros into vacated bit positions. Right-shift instructions come in logical and arithmetic varieties, where a logical shift places zeros into vacated bit positions and an arithmetic shift fills vacated bits with the value contained in the (unshifted) most-significant bit, in order to avoid changing the sign of a two’s complement integer. Finally, many architectures support rotate instructions that treat the value being shifted as a circular buffer: bits that “fall off” one end of the register are used to fill in vacated bit positions at the other end. Rotating semantics are not available to C/C++ programs and we will not consider them further.

### 3. INTEGER ARITHMETIC IN C AND C++

There is tension in the design of C and C++ between the desire for portability on one hand, and the desire for highly predictable and efficient execution of low-level code on the other hand. Therefore, in spite of C’s reputation for being a simple “portable assembly language,” its integer semantics are quite different from those provided by real assembly languages. The same is true of C++. The most important differences include the following.
Table I. Examples of C/C++ Integer Operations and Their Results

<table>
<thead>
<tr>
<th>Expression</th>
<th>Result</th>
</tr>
</thead>
<tbody>
<tr>
<td>UINT_MAX + 1</td>
<td>0</td>
</tr>
<tr>
<td>LONG_MAX + 1</td>
<td>undefined</td>
</tr>
<tr>
<td>INT_MAX + 1</td>
<td>undefined</td>
</tr>
</tbody>
</table>
| SHRT_MAX + 1            | SHRT_MAX+1 if INT_MAX > SHRT_MAX, 
                         | otherwise undefined                     |
| char c = CHAR_MAX; c++  | varies¹                                 |
| -INT_MIN                | undefined                               |
| (char) INT_MAX          | commonly -1                             |
| 1 << -1                 | undefined                               |
| 1 << 0                  | 1                                       |
| 1 << 31                 | commonly INT_MIN in ANSI C and 
                         | C++98; undefined in C99 and C++11²³    |
| 1 << 32                 | undefined                               |
| 1 / 0                   | undefined                               |
| INT_MIN % -1            | undefined in C11, 
                         | otherwise undefined in practice        |

¹The question is: Does c get “promoted” to int before being incremented? If so, the behavior is well-defined. We found disagreement between compiler vendors' implementations of this construct.
²Assuming that the int type uses a two’s complement representation.
³Assuming that the int type is 32 bits long.

—Many integer operations are not entirely portable across platforms due to implementation-defined behaviors.
—C/C++ perform pervasive implicit conversions between bitwidths and between signed and unsigned types.
—A number of arithmetic operations have undefined behavior.
—Since the carry and overflow flags are not exposed in the language, it is not straightforward for programmers to write code that detects overflow before or after it happens.

The rest of this section explores the consequences of the first three of these issues; the last is discussed in Section 4.2. Table I contains some concrete examples of the results of evaluating some C/C++ expressions.

3.1. Implementation-Defined Behaviors
An implementation-defined behavior in C/C++ is one where individual implementations are given freedom to make a choice, but this choice must be consistent and documented. A few of C/C++’s integer-related implementation-defined behaviors would break most nontrivial C programs if they deviated from the expected values (given in parentheses).

— the number of bits in a byte (eight),
— the representation used for signed integers (two’s complement),
— the result of converting an integer-typed value into a narrower signed integer-typed value, where the value cannot be represented in the new type (truncation, with the potential for converting a positive number into a negative number),
— the result of performing a bitwise operation on values with signed integer types (it is as if the operands were converted to unsigned integers of the same width, the bitwise operation was performed on them, and the result was cast back to the signed type).
Other behaviors do tend to vary across platforms and compilers. These include the signedness of the char type and the size of the short, int, and long types (expressed as a multiple of the size of a char). The int type in C99 is not required to hold values in excess of 32,767.

3.2. The Usual Arithmetic Conversions

Most integer operators in C/C++ require that both operands have the same type and, moreover, that this type is not narrower than an int. The collection of rules that accomplishes this is called usual arithmetic conversions. The full set of rules encompasses both floating point and integer values; here we will discuss only the integer rules. First, both operands are promoted.

- If an int can represent all values of the original type, the value is converted to an int; otherwise, it is converted to an unsigned int. These are called the integer promotions. All other types are unchanged by the integer promotions.

If the promoted operands have the same type, the usual arithmetic conversions are finished. If the operands have different types but either both are signed or both are unsigned, the narrower operand is converted to the type of the wider one.

If the operands have different types and one is signed and the other is unsigned, then the situation becomes slightly more involved. If the unsigned operand is narrower than the signed operand, and if the type of the signed operand can represent all values of the type of the unsigned operand, then the unsigned operand is converted to signed. Otherwise, the signed operand is converted to unsigned.

These rules can interact to produce counterintuitive results. Consider this function.

```c
int compare (void) {
    long a = -1;
    unsigned b = 1;
    return a > b;
}
```

For a C/C++ implementation that defines long to be wider than unsigned, such as GCC for x86-64, this function returns zero. However, for an implementation that defines long and unsigned to have the same width, such as GCC for x86, this function returns one. The issue is that on x86-64, the comparison is between two signed integers, whereas on x86, the comparison is between two unsigned integers, one of which is very large. Some compilers are capable of warning about code like this.

3.3. Unsigned Overflow

According to the C99 standard, the following holds.

- A computation involving unsigned operands can never overflow, because a result that cannot be represented by the resulting unsigned integer type is reduced modulo the number that is one greater than the largest value that can be represented by the resulting type.

Thus, the semantics for unsigned overflow in C/C++ are precisely the same as the semantics of processor-level unsigned overflow, as described in Section 2. As shown in Table I, UINT_MAX+1 must evaluate to zero in a conforming C and C++ implementation.

Unsigned overflow tends to lead to two kinds of problems in C/C++ programs. First, it is often the case that developers are simply not expecting values to overflow. For example, one of the infamous Therac-25 bugs [Leveson and Turner 1993] occurred when an 8-bit variable, which used a non-zero value to signal an error, wrapped around to zero, causing a crucial checking function to be bypassed. Second, since the sizes of
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integer types are implementation-defined, overflow will occur at different values on different platforms. For example, GCC evaluates 0UL-1 to $2^{64} - 1$ on x86-64 and to $2^{32} - 1$ on x86.

### 3.4. Signed Overflow and Other Undefined Behaviors

If a signed arithmetic operation overflows, for example, by evaluating INT_MAX+1, the behavior of the C implementation is **undefined**. According to the C99 standard, undefined behavior is defined as follows.

> “Behavior, upon use of a non-portable or erroneous program construct or of erroneous data, for which this International Standard imposes no requirements.”

In Internet parlance, this behavior is defined as follows.³

> “When the compiler encounters a given undefined construct it is legal for it to make demons fly out of your nose.”

Other operations with undefined behavior include shifting a value by at least as many bit positions as the bitwidth of the promoted type of the operand, shifting by a negative number of bit positions, and division by zero. Undefined behavior represents a significant departure from the semantics of processor-level operations, and our experience is that many developers fail to appreciate the full consequences of this. The rest of this section examines these consequences.

#### 3.4.1. Silent Breakage

A C or C++ compiler may exploit undefined behavior in optimizations that silently break a program. For example, a routine refactoring of Google’s Native Client software accidentally caused $1 << 32$ to be evaluated in a security check.⁴

The compiler—at this point under no particular obligation—simply turned the safety check into a nop. Four reviewers failed to notice the resulting vulnerability.

Another illuminating example is the code in Listing 1. In this program, the same computation ((INT_MAX+1) > INT_MAX) is performed twice with two different idioms. Recent versions of GCC, LLVM, and Intel’s C compiler, invoked at the -O2 optimization level, all print a zero for the first value (line 6) and a one for the second (line 7). In other words, each of these compilers considers INT_MAX+1 to be both larger than INT_MAX and also not larger, at the same optimization level, depending on incidental structural features of the code. The point is that when programs execute undefined operations, optimizing compilers may silently break them in non-obvious and not necessarily consistent ways.

#### 3.4.2. Time Bombs

Undefined behavior also leads to **time bombs**—code that works under today’s compilers but breaks unpredictably in the future as optimization

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technology improves. The Internet is rife with stories about problems caused by GCC’s ever-increasing power to exploit signed overflows. For example, in 2005, a principal PostgreSQL developer was annoyed that his code was broken by a recent version of GCC.\(^5\)

It seems that gcc is up to some creative reinterpretation of basic C semantics again; specifically, you can no longer trust that traditional C semantics of integer overflow hold . . .

This highlights a fundamental and pervasive misunderstanding: the compiler was not “reinterpreting” the semantics but rather was beginning to take advantage of leeway explicitly provided by the C standard.

In Section 6.5, we describe a time bomb in SafeInt [LeBlanc 2004], a library that is itself intended to help developers avoid undefined integer overflows. This operation had been compiled by GCC (and other compilers) into code that behaved correctly. However, a subsequent version of GCC (4.7) exposed the error, presumably because it optimizes the code more aggressively. We discovered this error using IOC and reported it to the developers, who fixed it within days [LeBlanc 2011].

3.4.3. Illusion of Predictability. Some compilers, at some optimization levels, have predictable behavior for some undefined operations. For example, at low optimization levels, C and C++ compilers typically give two’s complement semantics to signed overflows. It is, however, very unwise to rely on this, as the behavior can change when the compiler, the compiler version, or the compiler flags are changed.

3.4.4. Informal Dialects. Some compilers support stronger semantics than are mandated by the standard. For example, both GCC and Clang support a -fwrapv command line flag that forces signed overflow to have two’s complement behavior. In fact, the PostgreSQL developers responded to the incident above by adding -fwrapv to their build flags. They are now, in effect, targeting a nonstandard dialect of C.

3.4.5. Nonstandard Standards. Some kinds of overflow have changed meaning across different versions of the standards. For example, \(1<<31\) is implementation-defined in ANSI C and C++98, while being explicitly undefined by C99 and C11 (assuming 32-bit ints). Our experience is that awareness of this particular rule among C and C++ programmers is low.

A second kind of nonstandardization occurs with constructs such as INT_MIN÷1 which is—by our reading—well defined in ANSI C, C99, C++98, and C++11. However, we are not aware of a C or C++ compiler that reliably returns the correct result, zero, for this expression. The problem is that on architectures including x86 and x86-64, correctly handling this case requires an explicit check in front of every \(\%\) operation; compiler vendors are unwilling to impose this overhead on their users. The C standards committee has recognized the problem and C11 explicitly makes this case undefined.

4. TOOL DESIGN AND IMPLEMENTATION

IOC, depicted in Figure 1, has two main parts: a compile-time instrumentation transformation and a runtime handler. The transformation is a compiler pass that adds inline numerical error checks; it is implemented as a \(~1600\) LOC extension to Clang [2011], the C/C++ frontend to LLVM [Lattner and Adve 2004]. IOC’s instrumentation is designed to be semantically transparent for programs that conform to the C or C++ language standards, except in the case where a user requests additional checking for conforming but error-prone operations (e.g., wraparound with unsigned integer types).

The runtime library is linked into the compiler’s output and handles overflows as they occur; it is \( \sim 900 \) lines of C code.

This section discusses some of the key design choices made in the tool, including at what point in compilation to perform the instrumentation, how to perform the runtime overflow checks, the design of the runtime library, and how to efficiently recover from a detected overflow operation and continue execution.

4.1. Where to Put the Instrumentation Pass?

The IOC transformation operates on the abstract syntax tree (AST) late in the Clang frontend, after parsing, type-checking, and implicit type conversions have been performed. This is an appropriate stage for inserting checks because full language-level type information is available, but the compiler has not yet started throwing away useful information as it does during the subsequent conversion into the flat LLVM intermediate representation (IR).

In a previous iteration of IOC, we encoded the required high-level information into the IR (using IR metadata), which is much simpler to transform than the AST and also allows the transformation to be more naturally expressed as a compiler pass. Unfortunately, this proved to be unreliable and unnecessarily complicated, because it requires a substantial amount of C-level type information to be recorded with the IR in order to support a correct transformation. For example, the IR does not distinguish between signed and unsigned integer types (since the two’s complement representation is equivalent for both), but this distinction is essential for correct checking of integer overflows in C code. Similarly, some important operations (such as signed to unsigned casts) do not exist at the IR level. Also, the original transformation was further complicated by the lack of a one-to-one mapping between IR and AST nodes. In short, it is much less error-prone to do the instrumentation in the frontend, where all the required information is naturally available, despite the greater complexity of the representation.

4.2. Overflow Checks

Finding overflows in shift operations is straightforward: operand values are bound-checked and then, if the checks pass, the shift is performed. Checking for overflow in arithmetic operations is trickier; the problem is that a checked \( n \)-bit addition or subtraction requires \( n + 1 \) bits of precision, and a checked \( n \)-bit multiplication requires \( 2n \) bits of precision. Finding these extra bits can be awkward. There are basically three ways to detect overflow for an operation on two signed integers \( s_1 \) and \( s_2 \).

(1) **Precondition Test.** It is always possible to test whether an operation will wrap without actually performing the operation. For example, signed addition will wrap if and only if this expression is true:

\[
((s_2 > 0) \land (s_1 > (\text{INT\_MAX} - s_2))) \lor ((s_2 < 0) \land (s_1 < (\text{INT\_MIN} - s_2))).
\]
In pseudocode,

\[
\text{if (precondition) then}
\text{call failure handler}
\text{endif}
\text{result} = s_1 \text{ op } s_2.
\]

(2) **CPU Flag Postcondition Test.** Most processors contain hardware support for detecting overflow: following execution of an arithmetic operation, condition code flags are set appropriately. In the general case, it is problematic to inspect processor flags in portable code, but LLVM supports a number of intrinsic functions where, for example, an addition operation returns a structure containing both the result and an overflow flag. The LLVM backends, then, emit processor-specific code that accesses the proper CPU flag. In pseudocode,

\[
\text{(result, flag) = } s_1 \text{ checked op } s_2
\text{if (flag) then}
\text{call failure handler}
\text{endif}.
\]

(3) **Width Extension Postcondition Test.** If an integer datatype with wider bitwidth than the values being operated on is available, overflow can be detected in a straightforward way by converting \( s_1 \) and \( s_2 \) into the wider type, performing the operation, and checking whether the result is in bounds with respect to the original (narrower) type. In pseudocode,

\[
\text{result} = \text{extend}(s_1) \text{ op } \text{extend}(s_2)
\text{if (result < MIN || result > MAX) then}
\text{call failure handler}
\text{endif}.
\]

IOC supports both the precondition test and the CPU flag postcondition test; width extension seemed unlikely to be better than these options due to the expense of emulating 64-bit and 128-bit operations. Initially, we believe that the CPU flag postcondition checks would be far more efficient, but this proved not to be the case. Rather, using the flag checks has an uneven effect on performance. We evaluate this performance question experimentally, as follows.

### 4.2.1. Performance of the Overflow Checks

We studied the overall performance impact of IOC in order to answer two questions: How do the precondition test and the CPU flag postcondition test compare in terms of the overheads they introduce? And how much runtime overhead is introduced by using IOC?

To answer these questions, we compile SPEC CPU 2006 in three ways: (1) a baseline compilation using Clang with optimization options set for maximum expected performance; (2) checking for undefined integer overflows (shifts and arithmetic) using precondition checks; (3) checking for undefined integer overflows (shifts and arithmetic) using the CPU flag postcondition test. We then ran the benchmarks on a 3.4GHz AMD Phenom II 965 processor, using their “ref” inputs—the largest input data, use for portable SPEC runs—five times and use the median runtime. We configure the fault handler to return immediately instead of logging overflow behaviors. Thus, these measurements do not include I/O effects due to logging, but they do include the substantial overhead of marshaling the detailed failure information that is passed to the fault handler.

For undefined behavior checking using precondition checks, slowdown relative to the baseline ranges from \(-0.5\%\)–\(191\%\), that is, from a tiny accidental speedup to a threefold increase in runtime. The mean slowdown was \(44\%\). Using flag-based postcondition checks, slowdown ranged from \(0.4\%\)–\(95\%\), with a mean of \(30\%\). However, the
improvement was not uniform: out of the 21 benchmark programs, only 13 became faster due to the IOC implementation using CPU flags.

The explanation for the uneven benefits of the CPU flag checks can be found in the interaction between the overflow checks and the compiler’s optimization passes. The precondition test generates far too many operations, but they are operations that can be aggressively optimized by LLVM. On the other hand, the LLVM intrinsics supporting the flag-based postcondition checks are recognized and optimized by relatively few optimization passes, causing much of the potential performance gain due to this approach to be unrealized.

4.3. Runtime Library for Error Reporting

To produce informative error messages, IOC logs the source-code location corresponding to each inserted check, including the column number where the operator appeared. (Operating on the AST instead of the LLVM IR makes such logging possible, and Clang has outstanding error reporting.) Thus, users can disambiguate, for example, which shift operator overflowed in a line of code containing multiple shift operators. Also in service of readable error messages, IOC logs the types and values of the arguments passed to the operator; this is important for operators with multiple modes of failure, such as shift. For example, an error we found in OpenSSL was reported as

```
<\hash.c, (464:20)> : Op: >>, Reason :
  Unaligned Right Shift Error: Right operand is negative or is greater than or equal to the width of the promoted left operand.
  BINARY OPERATION: left (uint32): 4103048108 right (uint32): 32.
```

Based on the value of an environment variable, the IOC failure handler can variously send its output to STDOUT, to STDERR, to the syslog daemon, or it can simply discard the output. The syslog option is useful for codes that are sensitive to changes in their STDOUT and STDERR streams and for codes such as daemons invoked in execution environments where capturing their output would be difficult.

Finally, to avoid overwhelming users with repetitive error messages, the fault handler uses another environment variable to specify the maximum number of times an overflow message from any particular program point will be printed.

4.4. Efficient Error Recovery

When an overflow check is triggered, it is often desirable to continue program execution, which we call recovery. When running IOC on a code base, there are often multiple errors, many of which may be benign, and using recoverable checking to find these with a single run is useful. Unfortunately, our original implementation showed substantial slowdowns in error-free execution when making checks recoverable. To evaluate these overheads, we ran IOC with signed overflow and division-by-zero checks on the subset of SPEC CINT2006 benchmarks that did not trigger these errors dynamically. Adding more check types caused this subset to be too small to be interesting. With our initial implementation, the no-recovery case had an overhead of 15% on average while recoverable checking had an average overhead of 35%.

We used hardware performance counters, accessed via Intel’s VTune software [Intel 2013], to investigate the causes of the slowdowns. The first source of slowdown poor code layout, which degraded performance via bad cache interaction and frontend stalls. We resolved this issue by adding branch prediction metadata at the LLVM IR level, which the backend uses to guide block placement. By hinting that the checks are unlikely to be taken, the basic blocks containing calls to the runtime are not interspersed with the normal code. This issue did not arise with no-recovery checking because the code after the handler call is marked as unreachable, which similarly influences block layout.
Using performance counters, we verified that our fix resolved the layout problem. For example, in the SPEC benchmark 456.hmmer, we observed that the number of instruction-starved cycles (cycles where the frontend did not issue an instruction) was reduced by 43% and CPI decreased from 0.685 to 0.614. Indeed, this result in a significant improvement for this benchmark: down from 179s to 142s (from 53% to 21% slowdown, relative to the no-recovery case). Averaged across the benchmarks, this optimization brought recovery overhead down to 25%, significantly less than the original 35%.

The second problem we identified was that, to enable recovery, failure-handling blocks for inserted checks must branch back into the program execution, which hurts register allocation performance in error-free paths. In particular, the compiler backend has to allocate registers with the additional constraints imposed by the parameter preparation and the registers clobbered by the runtime call. We resolved this by changing the runtime functions to use a special “cold” calling convention which indicates the call clobbers as little as is possible for the architecture. This was a difficult change because it requires making the runtime functions save/restore all registers on entry/exit with handwritten assembly and because, while LLVM already supports “colddc” as part of the IR, it is treated the same as the normal C calling convention on all architectures. To handle the former, we use handwritten assembly wrappers for the normal C functions and plan on proposing a function attribute to cause the compiler to do this automatically (gcc has support for this). For the latter, we extended the LLVM x86 and x86_64 backends to treat colddc calls as not clobbering any registers.

Together, these changes reduced average overhead to 21% (only 6% over the no-recovery case), which is low enough to make checks recoverable by default. These optimizations would benefit any similar runtime check instrumentation: the first has already been adopted into mainline Clang for use in other sanitization checks, and there are plans to make use of the calling convention optimization once sufficient support has been added to LLVM.

5. IOC DEPLOYMENT

After the initial release of IOC, our findings and user feedback encouraged us to work to make IOC more widely available by working with the LLVM and Clang developers to include integer overflow checks in the official Clang code. During this process, we made a number of functionality, efficiency, and usability changes resulting in the first widely-available easy-to-use tools to enable developers to better reason about integer overflows in the software they build and use. As a result of these efforts, integer checking support is now available in LLVM/Clang. The first release including our integer checking features was version 3.3 in June 2013.

5.1. User Interface

In the original version of IOC, users specified what operations to check by requesting checks for violations of a particular version of the C standard. This was found to be cumbersome, and for the version of IOC deployed with Clang, we integrated our command line options with the `--fsanitize` family of options, providing users with a unified interface to a variety of dynamic checks. Example invocations include `--fsanitize=integer` (check all suspicious integer operations) and `--fsanitize=shift` (check only shift operations).

5.2. Improved Diagnostics

Diagnostics were improved to read more naturally and to match the formatting style of errors given statically by the Clang compiler. Here are two examples taken from ClamAV 0.97.6.
5.3. New Features

Besides changing the interface, we also introduced a number of features to make IoT suitable for a larger variety of use cases. First, we added the ability to continue program execution (to “recover”) after a failing check. Making checks recoverable incurs a performance penalty, which we significantly reduced through the changes described in Section 4.4.

In addition to making checks recoverable, once a particular check has been triggered, often users are not interested in seeing the error again. This error deduplication was originally accomplished using a hash table, but this design proved to not scale well. To address this, we now emit a byte for each check which is used to track its triggered state which scales well and only requires a minor amount of additional space in the resulting binary.

Finally, often integer overflows are known to be intentional or the programmer has investigated it and determined it to be acceptable. To address these use cases while still being useful in reporting undesired integer overflows, a whitelist functionality was introduced to enable users to specify certain files or functions that should not be checked.

5.4. Runtime Concerns

Inclusion in a production compiler required us to address a number of edge-cases in our runtime that we previously ignored in our research prototype. Examples include avoiding dependencies on libc, linking the IoT runtime automatically and transparently in a variety of build scenarios, ensuring correct execution in multithreaded environments, and ensuring the preservation of errno across calls into the runtime functions.

6. INTEGER OVERFLOW STUDY

This section presents the qualitative and quantitative results of our study of overflow behaviors in C and C++ applications.

6.1. Limitations of the Study

There are necessarily several limitations in this kind of empirical study. Most important, because IoT is based on dynamic checking, bugs not exercised by our inputs will not be found. In this sense, our results underestimate the prevalence of integer numerical errors as well as the prevalence of intentional uses of wraparound in these programs.

A stress testing campaign, or use of a formal methods tool, would be very likely to uncover additional integer bugs.

Second, our methodology for distinguishing intentional from unintentional uses of wraparound is manual and subjective. The manual effort required meant that we could only study a subset of the errors: we focused on the errors in the SPEC CINT2000 benchmarks for these experiments. For the other experiments, we study a wider range of programs.
Finally, while some parts of our study are methodical, other parts were performed in an informal fashion, and hence our reporting of the results is also informal. For example, in many cases, we lack links into bug reporting systems because the programs that we tested do not have bug reporting systems—the bugs we found were reported by email. In other cases, we lack a definitive answer about whether a particular bug was fixed because (1) the developers failed to confirm that they fixed the bug and (2) the changes across versions of programs are often large enough that manually tracking the status of bugs in the code is infeasible.

6.2. A Taxonomy for Overflows
Table II summarizes our view of the relationship between different integer overflows in C/C++ and the correctness of software containing these behaviors. Only Type 2 overflows do not introduce numerical errors into carefully written C/C++ software. Using IOC, we have found examples of software errors of Types 1, 3, and 4, as well as correct uses of Type 2. The section numbers in the table are forward references to discussions of bugs in the next section. We found many additional examples of each type of error, but lack space to discuss them in detail.

6.3. Wraparound and Overflow in SPEC CINT 2000
To investigate the prevalence of, and use-cases for, overflows and wraparounds, we examined SPEC CINT2000 in detail. The SPEC benchmark suites each contain a carefully selected set of C and C++ programs, designed to be representative of a wide range of real-world software (and many like GCC, bzip2, and povray are taken from widely used applications). Moreover, since they are primary performance benchmarks for both compilers and architectures, these benchmarks have been compiled and tested with most optimizing compilers, making them especially good case studies.

We ran the SPEC benchmarks’ “ref” datasets. Using IOC, we investigated every addition, subtraction, multiplication, and division overflow in an attempt to understand what it is that developers are trying to accomplish when they put overflows into their code.

Our findings are shown in Table III and described next. This benchmark suite consists of 12 medium-sized programs (2.5–222 KLOC), eight of which executed integer overflows while running on their reference input datasets.

Note. Real C code can be messy. We have cleaned up the SPEC code examples slightly when we deem this to improve readability and to not change the sense of the code.
6.3.1. 164.gzip. IOC reported eight wraparounds in this benchmark, all using well-defined unsigned operations. Of course, even well-defined operations can be wrong; we discuss an example of particular interest, shown in Listing 2. The POSIX `memcpy` function is undefined if its source and target memory regions overlap. To guard against invoking `memcpy` incorrectly, the code checks that \( e \) (number of bytes copied) is less than the distance between \( w \) and \( d \) (both offsets into a memory region). If this check fails, a slower memory copy that correctly handles overlap is invoked.

However, when \( d \geq w \), an unsigned overflow occurs resulting in an integer that is much greater than any potential value for \( e \), causing the safety check to pass even when the source and target regions overlap. This overflow was reported by IOC, and while investigating the report, we discovered this potential bug. Fortunately, the version of gzip used in this experiment is rather old (based on 1.2.4), and this issue has already been reported and fixed upstream\(^6\) as of version 1.4. Note that this bug existed in gzip as of 1993 and was not fixed until 2010. Furthermore, the initial fix was overkill and was later fixed\(^7\) to be the proper minimal condition to protect the `memcpy`. This illustrates the subtlety of overflow errors and serves as a good example of well-defined overflows leading to logic errors. In terms of the taxonomy in Table II, this wraparound is Type 4.

6.3.2. 175.vpr. This benchmark had four unsigned wraparounds caused by two similar implementations of random number generation. As shown in Listing 3, the developers documented their intentional use of unsigned integer wraparound. These wraparounds are well-defined and benign and represent an important idiom for high-performance code. They are Type 2.

6.3.3. 176.gcc. This benchmark had overflows at 48 static sites, some undefined and some well-defined. Listing 4 shows code that tries to compute the largest representable signed integer. HOST\_WIDE\_INT is an int and mode_width is 32, making the expression equivalent to \((1 << 31) - 1\). This expression is undefined in two different ways. First, in C99 it is illegal to shift a “1” bit into or past the sign bit. Second, assuming that the shift operation successfully computes \( \text{INT\_MIN} \), the subtraction overflow results. In our experience, this idiom is common in C and C++ code. Although compilers commonly give it the semantics that programmers expect, it should be considered to be a time bomb. A better way to compute \( \text{INT\_MAX} \) is using unsigned arithmetic. This overflow is Type 1.

6.3.4. 176.gcc. Listing 5 shows an undefined overflow that may cause GCC to generate suboptimal code even in the case where the signed overflow is compiled to a wraparound behavior. The variable `insn_count` is used as a score in a heuristic that decides whether to move a register outside of a loop. When it overflows, this score inadvertently goes from being very large to being small, potentially affecting code generation. This overflow is Type 3.

\(^6\)http://git.sv.gnu.org/gitweb/?p=gzip.git;a=commit;h=b9e94c93df914bd1d9eec9f150b2e4e00702ae7b.
\(^7\)http://git.sv.gnu.org/gitweb/?p=gzip.git;a=commit;h=17822e2cab5e47d73f224a688be8013c34f990f7.
<table>
<thead>
<tr>
<th>Name</th>
<th>Location</th>
<th>Op</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>164.gzip</td>
<td>bits.c(136:18)</td>
<td>+u</td>
<td>Bit manipulation</td>
</tr>
<tr>
<td>164.gzip</td>
<td>bits.c(136:28)</td>
<td>−u</td>
<td>Bit manipulation</td>
</tr>
<tr>
<td>164.gzip</td>
<td>deflate.c(540:21)</td>
<td>−u</td>
<td>Unused</td>
</tr>
<tr>
<td>164.gzip</td>
<td>inflate.c(558:13)</td>
<td>−u</td>
<td>Bit manipulation</td>
</tr>
<tr>
<td>164.gzip</td>
<td>inflate.c(558:22)</td>
<td>−u</td>
<td>Bit manipulation</td>
</tr>
<tr>
<td>164.gzip</td>
<td>inflate.c(566:15)</td>
<td>−u</td>
<td>Incorrect asm51 guard (Listing 2)</td>
</tr>
<tr>
<td>164.gzip</td>
<td>trees.c(552:25)</td>
<td>+u</td>
<td>Type promotion</td>
</tr>
<tr>
<td>164.gzip</td>
<td>trees.c(990:38)</td>
<td>−u</td>
<td>Type promotion</td>
</tr>
<tr>
<td>175.vpr</td>
<td>route.c(229:19)</td>
<td>−u</td>
<td>Hash</td>
</tr>
<tr>
<td>175.vpr</td>
<td>util.c(463:34)</td>
<td>+u</td>
<td>Random Number Generation (Listing 3)</td>
</tr>
<tr>
<td>175.vpr</td>
<td>util.c(463:39)</td>
<td>+u</td>
<td>Random Number Generation</td>
</tr>
<tr>
<td>175.vpr</td>
<td>util.c(484:34)</td>
<td>+u</td>
<td>Random Number Generation</td>
</tr>
<tr>
<td>175.vpr</td>
<td>util.c(484:39)</td>
<td>+u</td>
<td>Random Number Generation</td>
</tr>
<tr>
<td>176.gcc</td>
<td>combine.c × 6</td>
<td>−s</td>
<td>Find INT_MAX (Listing 4)</td>
</tr>
<tr>
<td>176.gcc</td>
<td>cse.c × 5</td>
<td>+u</td>
<td>Hash</td>
</tr>
<tr>
<td>176.gcc</td>
<td>expmed.c × 15</td>
<td>±u,s</td>
<td>Bit manipulation</td>
</tr>
<tr>
<td>176.gcc</td>
<td>expmed.c(2484:13)</td>
<td>+u</td>
<td>Inverse of x mod2^n</td>
</tr>
<tr>
<td>176.gcc</td>
<td>expmed.c(2484:18)</td>
<td>−u</td>
<td>Inverse of x mod2^n</td>
</tr>
<tr>
<td>176.gcc</td>
<td>expmed.c(2484:21)</td>
<td>+u</td>
<td>Inverse of x mod2^n</td>
</tr>
<tr>
<td>176.gcc</td>
<td>insn-emit.c(3613:5)</td>
<td>+u</td>
<td>Range check</td>
</tr>
<tr>
<td>176.gcc</td>
<td>loop.c(1611:19)</td>
<td>+s</td>
<td>Cost calculation bug (Listing 5)</td>
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<tr>
<td>176.gcc</td>
<td>m88k.c(127:44)</td>
<td>−u</td>
<td>Bit manipulation</td>
</tr>
<tr>
<td>176.gcc</td>
<td>m88k.c(128:20)</td>
<td>+u</td>
<td>Bit manipulation</td>
</tr>
<tr>
<td>176.gcc</td>
<td>m88k.c(128:20)</td>
<td>−u</td>
<td>Bit manipulation</td>
</tr>
<tr>
<td>176.gcc</td>
<td>m88k.c(888:13)</td>
<td>+u</td>
<td>Range check</td>
</tr>
<tr>
<td>176.gcc</td>
<td>m88k.c(1350:38)</td>
<td>+u</td>
<td>Range check</td>
</tr>
<tr>
<td>176.gcc</td>
<td>m88k.c(2133:9)</td>
<td>+u</td>
<td>Range check</td>
</tr>
<tr>
<td>176.gcc</td>
<td>obstack.c(271:49)</td>
<td>−u</td>
<td>Type promotion artifact</td>
</tr>
<tr>
<td>176.gcc</td>
<td>real.c(1909:35)</td>
<td>−u</td>
<td>Emulating addition</td>
</tr>
<tr>
<td>176.gcc</td>
<td>real.c(2149:18)</td>
<td>+s</td>
<td>Overflow check</td>
</tr>
<tr>
<td>176.gcc</td>
<td>rtl.c(193:16)</td>
<td>+u</td>
<td>Allocation cale bug (Listing 7)</td>
</tr>
<tr>
<td>176.gcc</td>
<td>rtl.c(193:16)</td>
<td>+u</td>
<td>Allocation cale bug (Listing 7)</td>
</tr>
<tr>
<td>176.gcc</td>
<td>rtl.c(216:19)</td>
<td>+u</td>
<td>Allocation cale bug (Listing 7)</td>
</tr>
<tr>
<td>176.gcc</td>
<td>rtl.c(216:5)</td>
<td>+u</td>
<td>Allocation cale bug (Listing 7)</td>
</tr>
<tr>
<td>176.gcc</td>
<td>stor-layout.c(1040:7)</td>
<td>−s</td>
<td>Find largest sint</td>
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<td>176.gcc</td>
<td>tree.c(1222:15)</td>
<td>+s</td>
<td>Hash</td>
</tr>
<tr>
<td>176.gcc</td>
<td>tree.c(1585:37)</td>
<td>−s</td>
<td>Bit manipulation</td>
</tr>
<tr>
<td>176.gcc</td>
<td>varasm.c(2255:15)</td>
<td>+s</td>
<td>Hash</td>
</tr>
<tr>
<td>186.crafty</td>
<td>evaluate.c(594:7)</td>
<td>−u</td>
<td>Bit manipulation</td>
</tr>
<tr>
<td>186.crafty</td>
<td>evaluate.c(595:7)</td>
<td>−u</td>
<td>Bit manipulation</td>
</tr>
<tr>
<td>186.crafty</td>
<td>iterate.c(436:16)</td>
<td>+s</td>
<td>Statistic bug (100*a/(b+1))</td>
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<tr>
<td>186.crafty</td>
<td>utility.c(813:14)</td>
<td>+u</td>
<td>Random Number Generation</td>
</tr>
<tr>
<td>197.parser</td>
<td>and.c × 6</td>
<td>+u,s</td>
<td>Hash</td>
</tr>
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<td>197.parser</td>
<td>fast-match.c(101:17)</td>
<td>+u</td>
<td>Hash</td>
</tr>
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<td>197.parser</td>
<td>fast-match.c(101:8)</td>
<td>+s</td>
<td>Hash</td>
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<tr>
<td>197.parser</td>
<td>parse.c × 10</td>
<td>+u,s</td>
<td>Hash</td>
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<td>197.parser</td>
<td>prune.c × 7</td>
<td>+u,s</td>
<td>Hash</td>
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<tr>
<td>197.parser</td>
<td>xalloc.c(68:40)</td>
<td>+u</td>
<td>Compute SIZE_MAX &gt;&gt; 1 (Listing 8)</td>
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<tr>
<td>197.parser</td>
<td>xalloc.c(70:19)</td>
<td>+u</td>
<td>Compute SIZE_MAX &gt;&gt; 1 (Listing 8)</td>
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(Continued)
Table III. Continued

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<thead>
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<th>Name</th>
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<th>$^{2}$Op</th>
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<tr>
<td>253.perlbmk</td>
<td>hv.c × 7</td>
<td>$^{+}$u</td>
<td>Hash</td>
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<tr>
<td>253.perlbmk</td>
<td>md5c.c × 68</td>
<td>$^{+}$u</td>
<td>Hash</td>
</tr>
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<td>253.perlbmk</td>
<td>pp.c(1958:14)</td>
<td>$^{-}$u</td>
<td>Missing cast</td>
</tr>
<tr>
<td>253.perlbmk</td>
<td>pp.c(1971:6)</td>
<td>$^{+}$u</td>
<td>Missing cast</td>
</tr>
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<td>253.perlbmk</td>
<td>regcomp.c(353:26)</td>
<td>$^{+}$s</td>
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</tr>
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<td>253.perlbmk</td>
<td>regcomp.c(462:21)</td>
<td>$^{+}$s</td>
<td>Unused</td>
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<td>253.perlbmk</td>
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<td>Unused</td>
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<td>253.perlbmk</td>
<td>regcomp.c(465:34)</td>
<td>$^{+}$s</td>
<td>Unused</td>
</tr>
<tr>
<td>253.perlbmk</td>
<td>regcomp.c(465:9)</td>
<td>$^{+}$s</td>
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<td>253.perlbmk</td>
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<td>$^{+}$s</td>
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<tr>
<td>253.perlbmk</td>
<td>regcomp.c(585:13)</td>
<td>$^{+}$s</td>
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</tr>
<tr>
<td>253.perlbmk</td>
<td>sv.c(2746:19)</td>
<td>$^{-}$u</td>
<td>Type promotion artifact</td>
</tr>
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<td>254.gap</td>
<td>eval.c(366:34)</td>
<td>$^{+}$s</td>
<td>Overflow check requiring -fwrapv</td>
</tr>
<tr>
<td>254.gap</td>
<td>ident.c × 4</td>
<td>$^{+}$u</td>
<td>Hash</td>
</tr>
<tr>
<td>254.gap</td>
<td>integer.c × 28</td>
<td>$^{+}$s</td>
<td>Overflow check requiring -fwrapv</td>
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<td>254.gap</td>
<td>integer.c × 4</td>
<td>$^{+}$s</td>
<td>Overflow check requiring -fwrapv</td>
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<tr>
<td>254.gap</td>
<td>integer.c × 4</td>
<td>$^{-}$s</td>
<td>Overflow check requiring -fwrapv</td>
</tr>
<tr>
<td>255.vortex</td>
<td>ut.c(1029:17)</td>
<td>$^{+}$u</td>
<td>Random Number Generation</td>
</tr>
</tbody>
</table>

1Source, and line:column. For space, we summarize frequent ones as ‘× n’.
2Operation Type(s), and Signed/Unsigned.

Listing 3. Correct wraparound in a pseudorandom number generator in 175.vpr.

```c
#define IA 1103515245u
#define IC 12345u
#define IM 2147483648u

static unsigned int c_rand = 0;

/* Creates a random integer [0...imax] (inclusive) */
int my_irand (int imax) {  
    int ival;
    /* c_rand = (c_rand * IA + IC) % IM; */
    c_rand = (c_rand * IA + IC) % IM; // Use overflow to wrap
    ival = c_rand % (IM - 1); // Modulus */
    ival = (int)((float) ival + (float) (imax + 0.999) / (float) IM);
    return ival;
}
```

Listing 4. Undefined overflow in 176.gcc to compute INT_MAX.

```c
/* (unsigned) <= 0x7fffffff is equivalent to >= 0. */
#else if ((const_op == (HOST_WIDE_INT) 1 << (mode_width - 1)) == 1)
{
    const_op = 0, op1 = const0_rtx;
    code = GE;
}
```

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Listing 5. Overflow in loop hoisting cost heuristic in 176.gcc.

```c
if (moved_once[regno])
{
  insn_count += 2;
  ...
  if (already_moved[regno]
    || (threshold * savings + m->lifetime) >= insn_count
    || (m->forces & m->forces->done
     && n_times_used[m->forces->regno] == 1))
  {
    ...
  }
```


```c
#define POWER_OF_2_or_0(I) (((I) & (unsigned)(I - 1)) == 0)
int integer_ok_for_set (value)
{
  register unsigned value;
  /* All the "one" bits must be contiguous.
   * If so, MASK + 1 will be a power of two or zero. */
  register unsigned mask = (value | (value - 1));
  return (value & POWER_OF_2_or_0 (mask + 1));
}
```

Listing 7. Wraparound in an allocation function in 176.gcc.

```c
/* Allocate a zeroed rtvec vector of N elements */
rtvec rtvec_alloc (int n) {
  rtvec rt;
  int i;
  rt = (rtvec) obstack_alloc (rtl_obstack,
    sizeof (struct rtvec_def)
    [ ((n - 1) sizeof (rtunion))];
  ...
  return rt;
}
```

6.3.5. 176.gcc. Listing 6 shows code that determines properties about the integer passed in at a bit level. In doing so, it invokes various arithmetic operations (subtraction, addition, and another subtraction in the POWER_OF_2_or_0 macro) that wrap around. These are all on unsigned integers and are carefully constructed to test the correct bits in the integers, so all of these wraparounds are benign. This example is a good demonstration of safe bit-level manipulation of integers, a popular cause of wraparound in programs. This overflow is Type 2.

6.3.6. 176.gcc. In Listing 7, we see an allocation wrapper function that allocates a vector of n elements. It starts with 16 bytes and then adds \((n - 1) \times 8\) more to fill out the array, since the beginning rtvec_def struct has room for 1 element by default. This works well enough (ignoring the type safety violations) for most values of \(n\), but has curious behavior when \(n = 0\). Of course, since we are using a dynamic checker, we know that it is actually called with \(n = 0\) during a SPEC benchmarking run.

First, consider this code after the sizeof operators are resolved and the promotion rules are applied: 16 + (((unsigned)(n-1)) * ((unsigned)8)). When \(n = 0\), we immediately see the code casting \(-1\) to unsigned, which evaluates to UINT_MAX, or \(2^{32} - 1\). The result is then multiplied by eight, which overflows with a result of \(2^{32} - 8\). Finally,
void initialize_memory (void) {
  SIZE i, j;
  ...
  for (i=0, j=1; i < j; i = j, j = (2*j+1))
    largest_block = i;
  largest_block &= ALIGNMENT_MASK;
  // must have room for a nudge too
  largest_block = -sizeof(Nudge);
}

Listing 8. Computation of SIZE_MAX >> 1 in 197.parser.

the addition is evaluated, which produces the final result of 8 after wrapping around again.

Although the overflow itself is benign, its consequences are unfortunate. Only 8 bytes are allocated but the rtvec_def structure is 16 bytes. Any attempt to copy it by value will result in a memory safety error, perhaps corrupting the heap. This is one of the more intricate well-defined but ultimately harmful overflows that we saw; it is Type 4.

6.3.7. 186.crafty. In this benchmark, we found some Type 2 wraparounds in evaluate.c used to reason about a bitmap representation of the chessboard. Additionally, there is a Type 3 statistic miscalculation that seems like a minor implementation oversight.

6.3.8. 197.parser. This benchmark had a number of overflows, including undefined signed overflows in a hash table, as indicated in Table III. Here we focus on an overflow in 197.parser's custom memory allocator, shown in Listing 8. This loop computes SIZE_MAX, setting largest_block to SIZE_MAX >> 1. Unsigned overflow is used to determine when j exceeds the capacity of size_t (note that i = j when the loop terminates). While SIZE_MAX was not introduced until C99, it is unclear why sizeof and a shift were not used instead. This overflow is Type 2: well-defined and benign.

6.3.9. 254.gap. Most of the undefined signed overflows in the SPEC 2000 suite are currently latent: today's compilers do not break them by exploiting the undefinedness. 254.gap is different: today's compilers cause it to go into an infinite loop unless two's complement integer semantics are forced. From the LLVM developers' mailing list\(^8\), the following.

“This benchmark thinks overflow of signed multiplication is well defined. Add the \(\text{-fwrapv}\) flag to ensure that the compiler thinks so too.”

We did not investigate the errors in this benchmark due to the complex and obfuscated nature of the code. However, as shown in Table III, our tool reported many sources of signed wraparound as expected. The signed overflows are Type 1, as they rely on undefined behavior, and there is no mention of \(\text{-fwrapv}\) in the documentation or source code. Using \(\text{-fwrapv}\) would make this Type 2, but non-portable because it would be limited to compilers that support the \(\text{-fwrapv}\) flag.

6.3.10. Shift Overflows. In our examination of SPEC CINT2000, we also checked for shift errors, finding a total of 93 locations that overflow. Of these, 43 were 1 << 31, which is an idiom for INT_MIN that is legal in ANSI C, and another 38 were left shifts with a negative left operand which is also legal in ANSI C. For space reasons, and because this behavior is fairly benign (and well-defined until C99), these are omitted from Table III and not discussed in detail.

Summary of Overflows in SPEC CINT2000. As shown in Table III, we found a total of 219 static sources of overflow in eight of the 12 benchmarks. Of these, 148 were

using unsigned integers, and 71 were using signed integers (32%). Overall, the most common uses of overflow were for hashing (128), overflow check requiring fwrapv (37), bit manipulation (25), and random number generation (6). Finally, the vast majority of overflows found (both unsigned and signed) were not bugs, suggesting occurrence of integer overflow by itself is not a good indicator of a security vulnerability or other functional error.

6.4. Latent Undefined Overflows: Harmless, or Time Bombs?

The presence of integer overflows that result in undefined behavior in a well-worn collection of software like SPEC CINT raises the question: Do these overflows matter? After all—with the notable exception of 254.gap—the benchmarks execute correctly under many different compilers. For each undefined overflow site in a benchmark program that executes correctly, there are two possibilities. First, the values coming out of the undefined operation might not matter. For example, a value might be used in a debugging printout, it might be used for inconsequential internal bookkeeping, or it might simply never be used. The second possibility is that these overflows are time bombs: undefined behaviors whose results matter but that happen—as an artifact of today’s compiler technology—to be compiled in a friendly way by all known compilers.

To find the time bombs, we altered IOC’s overflow handler to return a random value from any integer operation whose behavior is undefined by the C or C++ standard. This creates a high probability that the application will break in an observable way if its execution actually depends on the results of an undefined operation. Perhaps amusingly, when operating in this mode, IOC is still a standards-conforming C or C++ compiler—the standard places no requirements on what happens to a program following the execution of an operation with undefined behavior.

SPEC CINT is an ideal testbed for this experiment because it has an unambiguous success criterion: for a given test input, a benchmark’s output must match the expected output. The results appear in Table IV. In summary, the strict shift rules in C99 and C++11 are routinely violated in SPEC 2006. A compiler that manages to exploit these behaviors would be a conforming implementation of C or C++, but nevertheless would create SPEC executables that produce incorrect results because it would expose the latent errors lurking in these codes.

### 6.5. Integer Overflows in the Wild

To understand the prevalence of integer overflow behaviors in modern open-source C and C++ applications, we ran IOC on a number of popular applications and libraries.
In all cases, we simply compile the system using IOC and then ran its existing test suité (i.e., we typed “make check” or similar). For this part of our work, we focused on undefined behaviors as opposed to well-defined wraparounds. Also, we explicitly avoided looking for bugs based on the stricter C99 and C++11 shift rules; developer awareness of these rules is low and our judgment was that bug reports about them would be unwelcome.

6.5.1. SQLite. SQLite is a compact DBMS that is extremely widely used: it is embedded in Firefox, Thunderbird, Skype, iOS, Android, and other systems. In March 2011, we reported 13 undefined integer overflows in the then-current version. Although none of these undefined behaviors were believed to be sources of bugs at the time, some of them could have been time bombs. The main developer promptly fixed all of these overflows and IOC found no problems in the next version.

IOC also found a lossy conversion from unsigned int to signed int that resulted in a negative value being used as an array index. This code was triggered when SQLite attempted to process a corrupted database file. The SQLite developer also promptly fixed this issue.9

6.5.2. SafeInt and IntegerLib. SafeInt [LeBlanc 2004] is a C++ class for detecting integer overflows; it is used in Firefox and also “used extensively throughout Microsoft, with substantial adoption within Office and Windows.” We tested SafeInt and found 43 sites at which undefined overflows occurred, about half of which were negations of INT_MIN. The SafeInt developers were aware that their code performed this operation but did not feel that it would have negative consequences. However, newer versions of G++ do in fact exploit the undefinedness of -INT_MIN, and we found that when SafeInt was built with this compiler, it returned incorrect results for some inputs. Basically, the G++ optimizer finally triggered this time bomb that had been latent in SafeInt for some time. We informed the developers of this issue and they promptly released a new version of SafeInt that contains no undefined integer behaviors.

We tested another safe integer library, IntegerLib [CERT 2006], which was developed by CERT. This library contains 20 sites at which undefined integer overflows occur. One of them is shown in Listing 9; it is supposed to check if arguments lhs and rhs can be added without overflowing. However, at Line 3, the arguments are added without being checked, a bug that results in undefined behavior. A reasonable workaround for this case would be to cast the arguments to an unsigned type before adding them.

6.5.3. Other Codes. Six overflows in the GNU MPC library that we reported were promptly fixed. We reported 30 overflows in PHP; subsequent testing showed that 20 have been fixed. We reported 18 overflows in Firefox, 71 in GCC, 29 in PostgreSQL, 5 in LLVM, and 28 in Python. In all of these cases, developers responded in a positive fashion, and in all cases except Firefox and LLVM, we subsequently received confirmation


```c
int addsi (int lhs, int rhs) {
    errno = 0;
    if (((((lhs+rhs)>>1hs) & ((lhs+rhs)>>1hs))
    >>= (sizeof(int)*CHAR_BIT-1)) {
        error_handler("OVERFLOW ERROR", NULL, EOVERFLOW);
        errno = EINVAL;
    }
    return lhs+rhs;
}
```
that at least some of the overflows had been fixed. Finally, we reported nine undefined
overflows in the GNU Multiple Precision Arithmetic Library, one in BIND, and one in
OpenSSL. We received no response from the developers of these three packages.

Out of all the codes we tested, only three were completely free of undefined integer
overflows, at least when tested with their own test suites: Kerberos, libpng, and libjpeg.
All three of these packages have had security vulnerabilities in the past; undoubtedly
the more recent versions that we tested have been subjected to intense scrutiny.

6.6. Automated Testing for Overflows

Building on our manual investigation of integer overflow in common C and C++ ap-
plications, we also conducted an automated investigation of integer overflows found
in a large number of popular open-source software packages. Using a list of the most
popular Debian packages, we built each package from source and recorded overflows
that occurred during this process. We explain the details of our experiment next, and
a summary of our findings is given in Table V.

6.6.1. Methodology. We built an automated testing system, making use of work by
Ledru\(^\text{10}\) to help coerce packages to use a custom compiler in their build process. We
modified IOC to integrate well into our build automation, enabling us to capture a list of
integer overflows that occurred while running each package with the test suite included
with the package. The errors reported are only from the source for each package and
do not include dependencies which were provided from binary packages.

Using this system, we built the top 10,000 source packages from Debian as posted to
the popcon website\(^\text{11}\) as of February 20th, 2013. This list captures the top source pack-
ages used regularly by Debian users with the popularity-contest package installed. We
used packages available in the more recent “sid” distribution of Debian and leveraged
Debian’s build infrastructure to build each package with IOC and to run any tests
included as part of the Debian packaging. In this way, we were able to look at a large
number of packages automatically.

Most of the 10,000 packages we analyzed were not valid for purposes of this experi-
ment: 1,560 failed to complete the build process successfully when using our compiler,
and of the remaining, only 3,371 contained invocations of code built by our compiler.
We determined this by modifying IOC to add to every translation unit processed a
global constructor that writes a log entry when and if the resulting code is loaded. This
log entry includes details of the command line used to run this software (as read from
\(\text{/proc/self/cmdline}\)). Using this modification, we found many packages only loaded
instrumented code as part of tests used during the configure process. To only count

\(^{10}\)http://clang.debian.net/.
\(^{11}\)http://popcon.debian.org/sourcemax/by_vote.
overflows reported in the software itself, we further filtered the set of valid packages (and the errors considered for each package) by those that load instrumented code outside of the configure step, leaving 1,172 packages that contained C/C++ code that were successfully built and had some semblance of being tested.

6.6.2. Results. A summary of our findings for these 1,172 packages is shown in Table V, and a more detailed list of packages is available online\footnote{http://wdtz.org/files/ioc-debian.log.} or upon request.

We found that 413 (35\%) of the packages triggered one of the recognized overflows, with 197 (16\% overall) involving undefined integer behavior. Unsigned overflow was found to be the most common overflow type, representing 85\% of the overflowing packages and 30\% overall. These numbers are a lower bound on the true prevalence of overflows as they represent only those overflows that occurred during the packaging process and is further limited by the often minor amount of testing this involves.

Of the packages listed in the top 10,000, we found undefined overflows other than shift errors in 78 packages. Errors found in each are shown in Table VI, broken down by type of undefined behavior encountered. Division errors did not occur in any of these packages. After shift errors, the most common type of undefined integer behavior was signed multiplication (64), followed by signed addition (32), signed negation (17), and signed subtraction (15).

We have not yet filed bugs for these but plan to do so after more investigation. Undefined behavior in these packages is particularly important to address due to the potentially high impact should these errors be the source of bugs or vulnerabilities.

In summary, we conducted a broad automated investigation of integer overflows in 1,172 of the top Debian packages and found 35\% triggered integer overflows and 16\% invoked undefined integer behavior. Moreover, these errors were discovered using only the relatively few test cases included with the packages during installation, without any serious testing for overflows. This highlights the prevalence of integer overflows in real software as well as how dangerously common undefined integer behavior is in software used every day by many users.

7. PERFORMANCE EVALUATION

As discussed in the previous section, incorrectly handling integers is a prevalent problem even in mature applications, and it is all too easy for a skilled developer to introduce a dangerous overflow bug. Similar to how tools such as Valgrind are commonly used today to mitigate the prevalence of often-subtle memory safety errors, we believe tools like IOC are critical to helping developers understand and mitigate the insidious occurrences of integer overflows.

One important measure of such a tool’s utility is the impact it has on performance. To evaluate the overheads, a developer can expect to see when using IOC, in this section, we report the overhead incurred on the SPEC CPU 2006 benchmarks and on two popular server applications, using a variety of checking configurations.

For all runs, we used IOC as it is most commonly used: recoverable checks, detailed reporting enabled, deduplication enabled. One result of this is that repeated overflows at the same source location include the costs of marshaling the details of the failed check, calling into the runtime, using an atomic exchange operation to handle deduplication, and printing details about an error the first time it is encountered. This differs from the experiments done in Section 4.2.1 in that we are evaluating the performance impact a user might experience, not evaluating alternative check implementations. These experiments were conducted on an otherwise idle 2.5GHz i7-2860QM with 16GB
RAM, with all dynamic scaling and multicore features disabled. Reported numbers are the median of at least three runs; we observed negligible variance within each set of runs.

### 7.1. Compiler

The compiler used for these experiments was chosen to be similar to what we expect users to have available. At the time of the experiment, not all features from our research prototype had been incorporated in the mainstream version of Clang, so for these experiments, we used a modified version of LLVM/Clang, based on r170089 (Dec 13th, 2012). Since then, full support has been added.

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<td>✗</td>
<td>✗</td>
<td>✗</td>
</tr>
<tr>
<td>libdap</td>
<td>✗</td>
<td>✗</td>
<td>✗</td>
<td>✗</td>
<td>vavoom</td>
<td>✗</td>
<td>✗</td>
<td>✗</td>
<td>✗</td>
</tr>
<tr>
<td>libgda5</td>
<td>✗</td>
<td>✗</td>
<td>✗</td>
<td>✗</td>
<td>vim</td>
<td>✗</td>
<td>✗</td>
<td>✗</td>
<td>✗</td>
</tr>
<tr>
<td>libimager-perl</td>
<td>✗</td>
<td>✗</td>
<td>✗</td>
<td>✗</td>
<td>wireshark</td>
<td>✗</td>
<td>✗</td>
<td>✗</td>
<td>✗</td>
</tr>
<tr>
<td>libmdecrypt</td>
<td>✗</td>
<td>✗</td>
<td>✗</td>
<td>✗</td>
<td>zsh-beta</td>
<td>✗</td>
<td>✗</td>
<td>✗</td>
<td>✗</td>
</tr>
<tr>
<td>liboggz</td>
<td>✗</td>
<td>✗</td>
<td>✗</td>
<td>✗</td>
<td>zsh</td>
<td>✗</td>
<td>✗</td>
<td>✗</td>
<td>✗</td>
</tr>
</tbody>
</table>
7.2. Check Configurations

The five check configurations used are as follows.

— **Full.** All checks are supported: signed, unsigned, shift, division, and both implicit and explicit conversion checks.

— **Full-Shift.** All checks are supported other than shift checking, as shifts are frequently misused which can lead to high overheads or a large number of false positives. In conjunction with Full, this also helps demonstrate the cost of checking these shifts.

— **Full-Explicit.** All check types are supported other than explicit conversions. These are often less interesting because they are well-understood due to straightforward semantics and clear visibility at the source level.

— **Default.** These checks are supported: signed, unsigned, shift, and division checks. This configuration is important as it is the default set of checks enabled when integer checking is enabled on a recent version of Clang. Support for conversion checks has not been added at the time of writing, and the default checks may change to include value-losing implicit conversions in the future.

— **Undefined.** Only checks for undefined integer behavior are supported: signed, shift, and division. Developers are generally most interested in seeing these kinds of errors reported.

7.3. SPEC CPU2006

The SPEC CPU benchmarks are CPU-bound, arithmetic-intensive programs, which means that they are likely to incur higher checking overheads than do many other common applications that are network-, disk- or UI-intensive. We instrumented the C/C++ benchmarks of SPEC CPU2006 with all five configurations of IOC, as well as a baseline configuration. Our results are shown in Table VII. The measurements used the ref inputs, as required for official performance measurements with SPEC. Each reported number is the median of at least three runs, with a relative standard deviation (i.e., the coefficient of variation) less than 0.08%. Depending on the checks enabled, the geometric mean overhead was between 20.0% (undefined only) and 28.8% (all checks enabled).

Comparing the Full and Default configurations, we observed that disabling all conversion checks improved performance by 4.2%. Removing the explicit conversion checks (Full vs. Full-Explicit) resulted in only 0.8% improvement. Disabling shift checks (Full vs. Full-Shift) only improved performance overall by 1%, but it had a significant impact on 464.h264ref because this program frequently misuses shift operations. Enabling only the undefined checks incurs an average overhead of less than 20%, with no benchmark incurring over 37% overhead. 464.h264ref saw a significant improvement here as well due to its use of unsigned overflow in tight loops. Overall, other than 464.264ref, all overheads were less than 60%.

7.4. Server Applications

In addition to the integer-intensive scenario presented by CINT2006, we evaluated the performance impact of IOC on two popular server applications: Apache httpd and OpenSSH’s sshd. For httpd, we measured average response time while under load for various file sizes using the three main checking configurations. Response times were measured over a gigabit link using the ab tool shipped with httpd, using 25 concurrent connections making as many requests as possible for 200 seconds. The experiment was repeated five times for each file size and configuration, with the mean values reported in Table VIII. The coefficients-of-variation for the 1KB experiments were 2.5%; others were less than 0.12%. Experiments for file sizes over 100KB were limited by the network, hence the lack of change across check configurations for these.
Table VII. Performance Overhead of IOC on the SPEC CPU 2006 Benchmark Suite

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Baseline Time (s)</th>
<th>Full Raw Time (s)</th>
<th>Full-Shift Raw Time (s)</th>
<th>Full-Explicit Raw Time (s)</th>
<th>Default Raw Time (s)</th>
<th>Undefined Raw Time (s)</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench</td>
<td>460.1</td>
<td>657.7</td>
<td>669.7</td>
<td>666.4</td>
<td>551.7</td>
<td>490.5</td>
</tr>
<tr>
<td>bzip2</td>
<td>641.4</td>
<td>926.2</td>
<td>940.4</td>
<td>954.0</td>
<td>842.7</td>
<td>749.4</td>
</tr>
<tr>
<td>gcc</td>
<td>361.7</td>
<td>407.6</td>
<td>406.0</td>
<td>402.8</td>
<td>399.1</td>
<td>379.1</td>
</tr>
<tr>
<td>mcf</td>
<td>326.4</td>
<td>363.1</td>
<td>363.3</td>
<td>362.4</td>
<td>361.5</td>
<td>361.3</td>
</tr>
<tr>
<td>gobmk</td>
<td>574.6</td>
<td>816.0</td>
<td>815.6</td>
<td>811.3</td>
<td>734.5</td>
<td>735.7</td>
</tr>
<tr>
<td>hmer</td>
<td>821.1</td>
<td>1120.4</td>
<td>1120.4</td>
<td>1135.5</td>
<td>1124.3</td>
<td>1117.4</td>
</tr>
<tr>
<td>sjeng</td>
<td>629.8</td>
<td>860.6</td>
<td>859.7</td>
<td>861.6</td>
<td>851.9</td>
<td>837.5</td>
</tr>
<tr>
<td>libquantum</td>
<td>416.6</td>
<td>570.4</td>
<td>602.3</td>
<td>570.5</td>
<td>569.0</td>
<td>569.0</td>
</tr>
<tr>
<td>h264ref</td>
<td>320.7</td>
<td>335.9</td>
<td>339.2</td>
<td>334.1</td>
<td>334.0</td>
<td>335.1</td>
</tr>
<tr>
<td>gcc</td>
<td>482.5</td>
<td>560.2</td>
<td>572.0</td>
<td>559.5</td>
<td>552.4</td>
<td>566.0</td>
</tr>
<tr>
<td>xalancbmk</td>
<td>271.6</td>
<td>305.3</td>
<td>305.4</td>
<td>296.8</td>
<td>292.3</td>
<td>282.4</td>
</tr>
<tr>
<td>milc</td>
<td>392.7</td>
<td>482.0</td>
<td>481.7</td>
<td>481.2</td>
<td>481.1</td>
<td>480.5</td>
</tr>
<tr>
<td>namd</td>
<td>481.4</td>
<td>555.9</td>
<td>556.1</td>
<td>556.0</td>
<td>557.3</td>
<td>555.5</td>
</tr>
<tr>
<td>dealII</td>
<td>376.6</td>
<td>574.2</td>
<td>570.8</td>
<td>565.8</td>
<td>565.6</td>
<td>369.6</td>
</tr>
<tr>
<td>soplex</td>
<td>271.2</td>
<td>318.7</td>
<td>316.8</td>
<td>316.8</td>
<td>316.8</td>
<td>314.7</td>
</tr>
<tr>
<td>povray</td>
<td>211.4</td>
<td>234.7</td>
<td>235.7</td>
<td>235.7</td>
<td>233.5</td>
<td>235.7</td>
</tr>
<tr>
<td>lbm</td>
<td>388.2</td>
<td>439.7</td>
<td>439.8</td>
<td>440.8</td>
<td>439.8</td>
<td>440.7</td>
</tr>
<tr>
<td>spix3</td>
<td>691.7</td>
<td>928.9</td>
<td>927.6</td>
<td>826.4</td>
<td>826.4</td>
<td>828.4</td>
</tr>
</tbody>
</table>

Geometric Mean: 28.8% 27.8% 27.9% 24.6% 19.9%

Table VIII. Mean Response Times of Apache’s httpd under Different IOC Configurations

<table>
<thead>
<tr>
<th>Req. Size</th>
<th>Baseline Sec/Req</th>
<th>Full Raw O/H</th>
<th>Default Raw O/H</th>
<th>Undefined Raw O/H</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 KB</td>
<td>0.780</td>
<td>0.807</td>
<td>0.780</td>
<td>0.778</td>
</tr>
<tr>
<td>10 KB</td>
<td>2.528</td>
<td>2.491</td>
<td>2.515</td>
<td>2.520</td>
</tr>
<tr>
<td>100 KB</td>
<td>22.034</td>
<td>22.035</td>
<td>22.035</td>
<td>22.034</td>
</tr>
<tr>
<td>1 MB</td>
<td>224.633</td>
<td>224.648</td>
<td>224.647</td>
<td>224.645</td>
</tr>
<tr>
<td>10 MB</td>
<td>2252.290</td>
<td>2253.480</td>
<td>2253.170</td>
<td>2252.890</td>
</tr>
</tbody>
</table>

Table IX. Performance Overhead of File Copy using Instrumented OpenSSH Server

<table>
<thead>
<tr>
<th>File Size</th>
<th>Baseline</th>
<th>Full</th>
<th>Full-Shift</th>
<th>Full-Explicit</th>
<th>Default</th>
<th>Undefined</th>
</tr>
</thead>
<tbody>
<tr>
<td>100 MB</td>
<td>3.09</td>
<td>3.22</td>
<td>3.22</td>
<td>3.22</td>
<td>3.12</td>
<td>3.10</td>
</tr>
<tr>
<td>1 GB</td>
<td>20.73</td>
<td>22.06</td>
<td>21.97</td>
<td>22.00</td>
<td>21.02</td>
<td>20.63</td>
</tr>
<tr>
<td>10 GB</td>
<td>189.60</td>
<td>204.00</td>
<td>203.10</td>
<td>202.30</td>
<td>190.60</td>
<td>190.30</td>
</tr>
</tbody>
</table>

However, even on the smaller file sizes, the check configurations had only a minor impact, with the performance differences comparable to the standard deviation for the 1KB and 10KB runs.

With sshd, we measured the performance impact by copying files from an instrumented server across a gigabit network using scp. Table IX shows the median of five runs for each configuration and file size. Coefficients-of-variations for the 100M experiments were less than 0.8%; the others were less than 1.6%. Under all configurations the server was CPU-bound (files were likely cached), while the client was not. Here the instrumentation slowed the transfer rates at most by 8%. No checks included in the “Default” or “Undefined” configurations triggered dynamically while conducting our scp experiments.
7.5. Performance Summary
We observed minimal performance impact on server applications regardless of the check configuration. Even on the CPU-intensive SPEC benchmarks, average overheads were relatively low (20.0% to 28.8%), making IOC suitable for use in nightly testing or as a debugging aid. We expect these overheads can be reduced more by further improving LLVM’s handling of overflow intrinsics in its optimization passes and by removing or simplifying checks through use of a range analysis. Regardless, we believe the overheads are already sufficiently low to make IOC usable for most C/C++ software today.

8. PRIOR WORK
Integer overflows have a long and interesting history. The popular Pac-Man game, released in 1980, suffered from two known integer overflows that generate surprising, user-visible artifacts [Hodges 2008; Wikipedia 2011b]. More recently, as buffer overflows in C and C++ programs have been slowly brought under control, integer overflows have emerged as an important root cause of exploitable vulnerabilities in Internet-facing programs [Christey et al. 2011; Christey and Martin 2007].

Solutions to integer overflow are almost as old as the problem. For example, the IBM 702 provided a hardware-based implementation of variable-precision integers more than 50 years ago [Wikipedia 2011a]. MacLisp, in the 1960s, provided the first widely-available software implementation of arbitrary precision arithmetic. Even so, for a variety of reasons, today’s low-level programming languages eschew well-known integer overflow solutions, forcing programmers to deal with modulo integers and undefined behaviors.

Although there has been extensive work, especially during the last decade or so, on tools and libraries for mitigating integer-based security vulnerabilities, none of these tools have been used to understand the patterns of integer numerical overflows in real-world programs and benchmarks, which is the main focus of our work. Instead, those efforts have focused primarily on developing new tools and libraries and evaluating their efficacy. In particular, none of these projects has specifically attempted to examine the prevalence of undefined behaviors, although there is data in some of these papers about specific bugs. Moreover, none of these projects has attempted to examine the prevalence of intentional wraparound behaviors, or the idioms for which they are used, except the limited data in the paper on RICH.

The RICH paper [Brumley et al. 2007] presents two relevant pieces of data. First, it classifies integer numerical errors from MITRE’s CVE database [MITRE Corporation 2013] as overflow, signedness, and truncation errors. This classification does not show how prevalent numerical errors are across programs, because the survey only looks at cases where overflows have already been reported, not a general collection of programs. Second, they briefly discuss some benign overflow behaviors that are flagged as errors by their tool and discuss for what algorithms those overflows are used. That study provides limited data about the prevalence and patterns of intentional uses because their goal was different—to evaluate false positives from RICH. We study the empirical questions systematically and in more detail.

Other prior research on mitigating integer-based security vulnerabilities is more tangential to our work. We briefly discuss that work to illustrate the solutions available. The tools vary from static analysis and dynamic instrumentation to libraries with various strategies to mitigate the problem.

RICH is a compiler-based tool that instruments programs to detect signed and unsigned overflows in addition to lossy truncations and sign-conversions [Brumley et al. 2007]. BRICK [Chen et al. 2009] detects integer overflows in compiled executables...
using a modified Valgrind [Nethercote and Seward 2003]. The runtime performance is poor (50X slowdown) and the lack of C-level type information in executable code causes both false positives and false negatives. SmartFuzz [Molnar et al. 2009] is also based on Valgrind but goes further by using whitebox testing to generate inputs leading to good test coverage. IntScope [Wang et al. 2009] is a static binary analysis tool for integer vulnerabilities.

The As-if Infinitely Ranged (AIR) integer model [Dannenberg et al. 2010] is an ambitious solution that is intended to be used in deployed software. It simply provides well-defined semantics for most of C/C++’s integer-related undefined behaviors. AIR provides a strong invariant—every integer operation either produces the mathematically correct result or else traps—while being carefully designed to minimally constrain the optimizer. An alternative online solution is provided by libraries such as SafeInt [LeBlanc 2004] and IntegerLib [CERT 2006], where checked operations must be explicitly invoked and overflows explicitly dealt with. SafeInt, however, is quite easy to use because it exploits C++’s exceptions and operator overloading.

9. CONCLUSION

We have conducted an empirical study of the prevalence and patterns of occurrence of integer overflows in C and C++ programs, both well-defined and undefined and both intentional and inadvertent. We find that intentional uses of wraparound behaviors are much more common than is widely believed, for example, over 200 distinct locations in SPEC CINT2000 alone. We identify a wide range of algorithms for which programmers use wraparound intentionally.

Unfortunately, we observe that some of the intentional uses are written with signed instead of unsigned integer types, triggering undefined behaviors in C and C++. Compilers are free to generate arbitrary results for such code. In fact, we identified a number of lurking time bombs that happen to work correctly with some of today’s compilers but may fail with future compiler changes, such as more aggressive optimizations. Finally, we identified a number of previously unknown numerical bugs in widely used open-source software packages (and even in safe integer libraries!), many of which have since been fixed or acknowledged as bugs by the original developers. Even among mature programs, only a small fraction are free of integer numerical errors.

Overall, based on the locations and frequency of numerical errors, we conclude that there is widespread misunderstanding of the (highly complex) language rules for integer operations in C/C++, even among expert programmers. Our results also imply that tools for detecting integer numerical errors need to distinguish intentional from unintentional uses of wraparound operations—a challenging task—in order to minimize false alarms.

ACKNOWLEDGMENTS

We thank Tennessee Carmel-Veilleux, Danny Dig, Ganesh Gopalakrishnan, Alex Groce, Mary Hall, Derek Jones, Swarup Sahoo, and the ICSE 2012 reviewers for their insightful comments on drafts of this article.

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Understanding Integer Overflow in C/C++


Received February 2015; accepted March 2015